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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/050,078	01/17/2002	01/17/2002 Toshiyuki Okumura		6610	
25227	7590 12/23/2004		EXAMINER		
MORRISON & FOERSTER LLP 1650 TYSONS BOULEVARD			JACKSON, CORNELIUS H		
SUITE 300	3 BOOLL VARD	ART UNIT	PAPER NUMBER		
MCLEAN, VA 22102			2828		

DATE MAILED: 12/23/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

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		Applicat	tion No.	Applicant(s)			
		10/050,	078	OKUMURA, TOSHIYUKI			
	Office Action Summary	Examine	er	Art Unit			
	<u> </u>		s H. Jackson	2828			
Period fo	The MAILING DATE of this commun or Reply	nication appears on ti	he cover sheet w	ith the correspondence addr	ess		
THE - Exte after - If the - If NC - Failu Any	ORTENED STATUTORY PERIOD F MAILING DATE OF THIS COMMUN nsions of time may be available under the provisions SIX (6) MONTHS from the mailing date of this come period for reply specified above is less than thirty (3 period for reply is specified above, the maximum st are to reply within the set or extended period for reply reply received by the Office later than three months ed patent term adjustment. See 37 CFR 1.704(b).	ICATION. s of 37 CFR 1.136(a). In no enunication. s0) days, a reply within the statutory period will apply and y will, by statute, cause the a	event, however, may a datutory minimum of thir will expire SIX (6) MON pplication to become At	reply be timely filed ty (30) days will be considered timely. ITHS from the mailing date of this comi 3ANDONED (35 U.S.C. § 133).	munication.		
Status							
1)⊠	Responsive to communication(s) file	ed on <i>01 April 2004</i> .					
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3) Since this application is in condition for allowance except for formal matters, prosecution as to the mer							
,—	closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposit	ion of Claims						
5)⊠ 6)⊠ 7)□	Claim(s) <u>19,22-24,30 and 34-60</u> is/a 4a) Of the above claim(s) <u>49-58</u> is/a Claim(s) <u>19,22-24,30,37-41 and 59</u> Claim(s) <u>34-36,42-48 and 60</u> is/are Claim(s) is/are objected to. Claim(s) are subject to restric	re withdrawn from co is/are allowed. rejected.	onsideration.		·		
Applicat	ion Papers						
,	The specification is objected to by the three drawing(s) filed on is/are		o) objected to	by the Examiner.			
	Applicant may not request that any obje						
11)	Replacement drawing sheet(s) including The oath or declaration is objected to						
Priority ι	ınder 35 U.S.C. § 119						
a)	Acknowledgment is made of a claim All b) Some * c) None of: 1. Certified copies of the priority 2. Certified copies of the priority 3. Copies of the certified copies application from the Internation	documents have be documents have be of the priority docun onal Bureau (PCT Ro	een received. een received in A nents have been ule 17.2(a)).	pplication No received in this National St	tage		
Attachmen	t(s)						
1) Notice 2) Notice 3) Information	ce of References Cited (PTO-892) ce of Draftsperson's Patent Drawing Review (Fromation Disclosure Statement(s) (PTO-1449 or Process)		Paper No(Summary (PTO-413) s)/Mail Date nformal Patent Application (PTO-1 	52)		

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DETAILED ACTION

Response to Arguments

1. Acknowledgment is made that applicant's Amendment, filed on 30 August 2004, has been entered. Upon entrance of the Amendment, claims 19 and 30 were amended, claims 16-18, 20, 21, 25-29 and 31-33 were canceled, and claims 37-60 were added. Claims 19, 22-24, 30 and 34-60 are now pending in the current application.

Election/Restrictions

2. Newly submitted claims 49-58 are directed to an invention that is independent or distinct from the invention originally claimed for the following reasons: Independent claim 49 requires an active layer consisting of two quantum well layers and one barrier layer and fails to require either the barrier layer to have a layer thickness of 4 nm or less or a ridge portion and a planar portion on opposite sides of the ridge portion.

Since applicant has received an action on the merits for the originally presented invention, this invention has been constructively elected by original presentation for prosecution on the merits. Accordingly, claims 49-58 are withdrawn from consideration as being directed to a non-elected invention. See 37 CFR 1.142(b) and MPEP § 821.03.

Specification

3. The lengthy specification has not been checked to the extent necessary to determine the presence of all possible minor errors. Applicant's cooperation is requested in correcting any errors of which applicant may become aware in the specification.

Drawings

4. Figures 19-21 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). Corrected drawings in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.121(d)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Allowable Subject Matter

- 5. Claims 19, 22-24, 30, 37-41 and 59 are allowed.
- 6. The following is a statement of reasons for the indication of allowable subject matter:

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Regarding claims 22-24, prior art fails to disclose, teach, or suggest, alone or in combination, a gallium nitride semiconductor device as claimed having cladding layer with a ridge surface and a planar surface and an active layer containing two to four quantum well layers and one to three barrier layers respectively, each interposed between the quantum well layers, and the one or each barrier layer has a layer thickness of 4 nm or less.

Regarding claims 19 and 30, prior art fails to disclose, teach, or suggest, alone or in combination, a gallium nitride semiconductor device as claimed being a self-oscillating semiconductor laser device, **see specification**, **page 26**, **lines 3-10**, and having an active layer containing two to four quantum well layers and one to three barrier layers respectively, each interposed between the quantum well layers, and the one or each barrier layer has a layer thickness of 4 nm or less.

Regarding claim 59, prior art fails to disclose, teach, or suggest, alone or in combination, a gallium nitride semiconductor device as claimed having a p-type cladding layer that forms at least part of a ridge structure and an active layer containing two to four quantum well layers and one to three barrier layers respectively, each interposed between the quantum well layers, and the one or each barrier layer has a layer thickness of 4 nm or less.

7. The indicated allowability of claims 34-36 is withdrawn in view of the newly discovered reference(s) to Nakamura et al. (Applicant's Admitted Prior Art Figs. 19 and 20). Rejections based on the newly cited reference(s) follow.

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Claim Rejections - 35 USC § 102

8. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 9. Claims 34-36, 42-48 and 60 are rejected under 35 U.S.C. 102(b) as being anticipated by Nakamura et al. (Applicant's Admitted Prior Art Figs. 19 and 20). Regarding claims 34 and 60, Nakamura et al. disclose a gallium nitride semiconductor light emitting device Figs. 19 and 20 having emission wavelengths within a band corresponding to ultraviolet to green, see specification, page 1, lines 15-23, comprising a semiconductor substrate 101, an active layer 107 having a quantum well structure and made of nitride semiconductor containing at least indium and gallium, see specification, page 2, lines 3-5, and a first/n-type cladding layer 105 and a second/ptype cladding layer 110 for sandwiching/(between which) the active layer 107 therebetween/(is disposed), wherein the active layer forms an oscillating section of the semiconductor laser device and consists of two to four quantum well layers and one to three barrier layers each interposed between the quantum well layers, and wherein one of the first and second cladding layers is a p-type layer, and the p-type cladding layer has a ridge portion/structure and a planar portion on opposite sides of the ridge portion/structure, see specification, page 1, line 15-page 2, line 14.

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Regarding claim 35, Nakamura et al. disclose the ridge has a width of about 1 $\,$ μm to 5 μm .

Regarding claim 36, Nakamura et al. disclose the planar portions have a film thickness of 0.05 μm to 0.5 μm .

Regarding claim 42, Nakamura et al. disclose each of the quantum well layers has electrons and holes uniformly distributed therein, since electrons and holes are physical properties of the material it is inherently uniformly distributed therein, but not throughout.

Regarding claim 43, Nakamura et al. disclose each quantum well layer has a layer thickness of 10 nm or less, see specification, page 2, line 11.

Regarding claim 44, Nakamura et al. disclose the active layer consists essentially of nitrogen, indium and gallium, see specification, page 2, lines 3-5.

Regarding claim 45, Nakamura et al. discloses all of the structural requirement of the claim, therefore it is inherent that the laser of Nakamura will operate in the manner of a self-oscillating laser.

Regarding claims 46 and 47, It is inherent that the laser device of Nakamura et al. would be connected to a driving circuit for injecting an electric current, hence the need for electrodes 112 and 113, and depending on the use of the laser device of Nakamura et al., such as for optical disks, it is inherent that the electric current is a modulated current having a modulation frequency of 300MHz, see specification, page 3, lines 18-25.

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Regarding claim 48, It is inherent that the optical output will modulate/vary when an electric current is injected thereto, due to a change in temperature of the laser device.

Claim Rejections - 35 USC § 103

- 10. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 11. Claims 46 and 47 are rejected under 35 U.S.C. 103(a) as being unpatentable over Nakamura et al. (5777350) in view of Larson (5767704). Nakamura et al., as applied to claim 34 above, teach all the stated limitations except for a driving circuit for injecting current into the semiconductor laser device wherein the electric current is modulated current and a modulation frequency of the current is 300 MHz or more. Larson teach a driving circuit for injecting current into the semiconductor laser device wherein the electric current is modulated current and a modulation frequency of the current is 300 MHz or more was well known in the art at the time the invention was made and was a requirement of the read mode to prevent the laser device from mode hopping, see col.

 1, lines 15-50. It would have been obvious to one of ordinary skill in the art at the time the invention was made to employ the driving circuit of Larson to the laser of Nakamura et al. to prevent the laser device from mode hopping.

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Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Cornelius H. Jackson whose telephone number is (571)272-1942. The examiner can normally be reached on 8:00 - 5:00, Monday - Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, MinSun Harvey can be reached on (571)272-1835. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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